Name	**Dr.L.JEBARAJ**			
Contact (office)	Professor in Electrical and Electronics Engineering Department of Electrical and Electronics M.I.E.T. Engineering College Trichy.			
Email	drjebaraj@miet.edu			
Educational Qualifications	**DEGREE**	**EDUCATIONAL INSTITUTION**	**BOARD/ UNIVERSITY**	**YEAR OF PASSING**
	Ph.D	Centre for Research	Anna University	2014
	M.E (Power Systems)	Faculty of Engineering and Technology	Annamalai University	2007
	UG (A.M.I.E)	The Institution of Engineers (India)	The Institution of Engineers (India)	1998
Professional Career Profile	**Organization**	**Designation**	**Date of Joining**	**Duration of work**
	M.I.E.T. Engineering College	Professor	20.03.2020	Till Date
	Loyola Institute of Technology	Professor & Head	06.06.2019	18.03.2020
	Mount Zion College of Engineering and Technology	Professor & Head	08.06.2018	30.11.2018
	M.I.E.T. Engineering College	Professor & Head	12.06.2014	30.04.2018
	St. Anne's College of Engineering and Technology	Associate Professor & Head	06.06.2012	06.06.2014
	V.R.S. College of Engineering and Technology	Assistant Professor	02.07.2007	30.04.2012
	Vivekanandha Polytechnic College	Lecturer	19.12.2003	05.09.2005
	Padaleeswarar Polytechnic College	Lecturer	08.04.1999	21.11.2003

Awards & Recognitions	❖ Recognized research supervisor of Anna University, Chennai. ❖ Fellow of the Institution of Engineers (India), Kolkata. ❖ Key note speaker for numerous National/International Conferences. ❖ Advisory committee member for many National/International Conferences ❖ Approved Reviewer for the following Journals ✓ Electrical Power and Energy Systems (Elsevier) ✓ Electrical Power Component and Systems (Taylor & Francis) ✓ IETE Journal of Research (Taylor & Francis) ✓ The International Journal for Computation and mathematics in Electrical Engineering (COMPEL – Emerald Publishers) ✓ IEEE Access (IEEE) ✓ IET Generation, Transmission and Distribution (IET)
	❖ Produced 100 % Results in the following subjects ✓ Flexible AC Transmission Systems ✓ Power System Transients ✓ Power Electronics for Renewable Energy Systems ✓ Fuzzy logic and Neural Networks
Membership in Professional Bodies	FELLOW (IEI); F-1233021 MEMBER (IEEE); M-93950707 MEMBER (IAENG-Hong Kong); M-266150
Academic Areas of Interest	✓ Applications of Optimization Techniques in Power System Problems ✓ Flexible AC Transmission Systems ✓ Voltage Stability ✓ Optimal Power Flow ✓ Unit Commitment ✓ Economic Load Dispatch
Administrative Capabilities	**Roles and Responsibilities in MIET Engineering College, Trichy** ✓ Former Head in Electrical and Electronics Engineering Department ✓ Former college Discipline Committee member ✓ Member in Research Committee **Administrative Duties involved at Loyola Institute of Technology** ✓ Head in Electrical and Electronics Engineering Department ✓ College Discipline Committee member ✓ Expert member for college recruitment process ✓ Member in College MSME Incubation Cell **Administrative Duties involved at St. Anne's College of Engineering and Technology** ✓ Head in Electrical and Electronics Engineering Department ✓ College Discipline Committee member ✓ Member – Student Affairs

List of JournalPublications:

1. **L.Jebaraj**, Muralikrishnan, and C. Christober Asir Rajan, "A comprehensive Review on Evolutionary Optimization Techniques applied for Unit Commitment Problem",***IEEE Access***, Vol. 8, No.1, pp: 132980 – 133014, 2020. **Impact Factor: 4.086**.

2. **L. Jebaraj**, I. Soubache and C. Christober Asir Rajan, "Performance Comparison of TCSC and split TCSC under ATC Enhancement through Plant Root Foraging Algorithm", ***Journal of Electrical Engineering***, Vol. 17, No.4, pp: 193 – 204, 2018. **Impact Factor: 0.483**.

3. **L. Jebaraj**, C. Venkatesan, I. Soubache, and C. Christober Asir Rajan, "Application of Differential Evolution Algorithm in Static and Dynamic Economic or Emission Dispatch Problem: A Review", ***Renewable and Sustainable Energy Reviews (Elsevier)***, Vol. 77, pp: 1205 – 1220, 2012. **Impact Factor: 12.12.**

4. **L. Jebaraj** and C. Christober Asir Rajan, "Voltage and Real Power Loss Analysis Incorporating Series – Shunt Combination of FACTS Controller Through Fruit fly Optimization", ***WSEAS Transactions on Power Systems***, Vol. 10, No.3, pp: 55 – 72, 2015. **Impact Factor: 0.296.**

5. **L. Jebaraj**, C. Christober Asir Rajan and K. Sriram, "Application of Firefly Algorithm in Voltage Stability Environment Incorporating Circuit Element Model of SSSC with Variable Susceptance Model of SVC",***Advances in Electrical Engineering (Hindawi)***, Vol. 2014, No.1, pp: 1 – 11, 2014.

6. **L. Jebaraj**, C. Christober Asir Rajan, K. Sriram, J. Ramesh, and R. Sivasankari, "SSSC and SVC Interaction on Voltage Stability Limit Enhancement and Active Power Loss Minimization through Differential Evolution Algorithm under Different Operating Conditions of a Power System", ***Scientific Research and Essays***, Vol. 8, No.24, pp: 1121 – 1133, 2013. **Impact Factor: 0.493.**

7. **L.Jebaraj**, Muralikrishnan, and C. Christober Asir Rajan, "Comparison between Two Different Combinations of FACTS Devices in Voltage Stability Limit Enhancement and Active Power Loss Minimization through Shuffled Frog Leaping Algorithm under Stressed Conditions",***Archives Des Sciences***, Vol. 65, No.11, pp: 180 – 195, 2012.**Impact Factor: 0.296**

8. **L.Jebaraj**, Muralikrishnan, and C. Christober Asir Rajan, "Voltage Stability Limit Enhancement and Active Power Loss Minimization Comparison between Two Different Combinations of FACTS Devices under Stressed Conditions", ***Wulfenia Journal***, Vol. 19, No.10, pp: 369 – 384, 2012. **Impact Factor: 0.267**.

9. **L.Jebaraj** and C. Christober Asir Rajan, "DE and SFLA Comparison in Voltage Stability Enhancement and Real Power Loss Minimization Incorporating SSSC and SVC Devices under Stressed Conditions of a Power System", ***Archives Des Sciences***, Vol. 65, No.9, pp: 76 – 90, 2012. **Impact Factor: 0.296.**

10. **L. Jebaraj** and C. Christober Asir Rajan, "Active Power Loss Minimization and Voltage Stability Limit Enhancement Comparison between DE and SFLA Incorporating TCSC and SVC Devices under Different Operating Conditions of a Power system", ***International Journal of Electrical Engineering Applications Research***, Vol. 3, No.3, pp: 167 – 176, 2012.

11. **L. Jebaraj**, C. Christober Asir Rajan and S. Sakthivel, "Real Power Loss and Voltage Stability Limit Optimization Incorporating TCSC and SVC through DE Algorithm under Different Operating Conditions of a Power System", ***IOSR Journal of Electrical and Electronics Engineering***, Vol. 1, No.5, pp: 16 – 25, 2012.

12. **L. Jebaraj**, C. Christober Asir Rajan and S. Sakthivel, "Performance Evaluation of TCSC and SVC on Voltage Stability Limit Improvement and Loss Minimization under Most Critical Line Outage Condition", ***International Journal of Engineering Research and Applications***, Vol. 2, No.3, pp: 3083 – 3090, 2012.

13. **L. Jebaraj**, C. Christober Asir Rajan and S. Sakthivel, "Shuffled Frog Leaping Algorithm based Voltage Stability Limit Improvement and Loss Minimization Incorporating FACTS Devices under Stressed Conditions", ***International Journal of Computer Applications***, Vol. 48, No.2, pp: 37 – 44, 2012.

14. **L. Jebaraj**, C. Christober Asir Rajan and S. Sakthivel, "Incorporation of SSSC and SVC Devices for Real Power and Voltage Stability Limit Enhancement through Shuffled Frog Leaping Algorithm under Stressed Conditions", ***European Journal of Scientific Research***, Vol. 79, No.1, pp: 119 – 132, 2012. **Impact Factor: 0.701.**

List of IEEE Conference Publications:

15. **L. Jebaraj** and M.Sammil, "Optimal Structures for Cascaded H-Bridge Inverter Topologies", ***IEEE International Conference on Circuit, Power and Computing Technologies***, pp: 1 – 8, 2016.
16. **L. Jebaraj** and E.Avinash, "A New Variable Frequency Inverted Sine Carrier PWM Modulated Semi-Cross Switched Multilevel Inverter Topologies", ***IEEE International Conference on Circuit, Power and Computing Technologies***,pp: 1 – 8, 2016.

17. **L. Jebaraj** and M. Keerthana, "A New Multi-Level DC Link Inverter Topology With Variable Frequency Inverted Sine Carrier PWM Under Equal Switching Transition", ***IEEE International Conference on Innovations in Information, Embedded and Communication Systems***, pp: 1 – 7, 2015.

18. **L.Jebaraj**, Muralikrishnan, and C. Christober Asir Rajan, "Comparison between Two Different Combinations of FACTS Devices in Voltage Stability Limit Enhancement and Real Power Loss Minimization through Shuffled Frog Leaping Algorithm under Single Line Outage Contingency Condition", ***IEEE International Conference on Power, Energy and Control***, pp: 1 – 8, 2014.

19. **L.Jebaraj**, Muralikrishnan, and C. ChristoberAsirRajan, "DE Algorithm based Comparison between Two Different Combinations of FACTS Devices under Single Line Outage Contingency Condition", ***IEEE International Conference on Intelligent Systems and Control***, pp: 158 – 165, 2013.